

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/759483	Docket Number	CNTR.2232
Filed	1/16/2004	Group Art Unit	2186
Examiner	Ryan Dare	Customer No.	23669
Application Title	MICROPROCESSOR WITH VARIABLE LATENCY STACK CACHE		
First Named Inventor	Rodney E. Hooker		

RESPONSE B – AMENDMENT

Mail Stop **Amendment**
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action of 8/24/2006, with a shortened statutory period of response set to expire 11/24/2006, please amend the above identified application as set forth below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

Conclusions begin on page 12 of this paper.